



#11/B 8/26/02
Atty. Dkt. No. 082259-0156

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Khosrow Golshan
Title: METHOD AND APPARATUS
FOR OPTICAL PROCESSING
Appl. No.: 09/630,883
Appl. Filing Date: 08/02/2000
Examiner: Chang, A.
Art Unit: 2872

CERTIFICATE OF MAILING I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on the date below. <u>MICHELE MATHES</u> (Printed Name) <u>Michele Mathes</u> (Signature) <u>8/12/2002</u> (Date of Deposit)

AMENDMENT

Commissioner for Patents
Box RCE
Washington, D.C. 20231

Sir:

Prior to examination of the present Request for Continued Examination, Applicant respectfully requests that the application be amended as follows:

In the Claims:

Marked up versions of the replacement claims as amended below have been included in Appendix A of this Amendment in accordance with 37 C.F.R. § 1.121.

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C1
B1
1. (Twice Amended) An optical logic circuit, comprising:
a substrate comprising a first material;
an optical layer overlaying the substrate at least partially comprising a second material, the optical layer configured to provide a plurality of optical pathways, at least one optical pathway configured to transmit an optical bias signal, at least one optical pathway configured to provide an optical input signal, and at least one optical pathway configured to provide an optical output signal; and
an interference region at least partially comprising the second material, configured to selectively cause interference of wavefronts of the optical bias signal and the optical input signal entering the interference region, the interference